THCV215 / THCV216 Evaluation Kit


V-by-One ${ }^{\circledR}$ HS Dual Link Evaluation Board
Parts Number: THEVA215-V2, THEVA216-V2

## 1. General Description

THEVA215-V2 and THEVA216-V2 are designed to evaluate THCV215 and THCV216 for transmission video data. Each has one THCV215 or THCV216. This kit can transmit video data of "Full-HD / 60Hz / 30bit" and "HD / 120Hz / 30Bit".

Table 1 Color Width

| Width | Link | LVDS Clock Freq. |
| :---: | :---: | :---: |
| 18bit | Single/Dual | 20 MHz to 100 MHz |
| 24 bit | Single/Dual | 20 MHz to 100 MHz |
| 32 bit | Single/Dual | 20 MHz to 85 MHz |
| 40bit | Single/Dual | 20 MHz to 75 MHz |

## 3. Overview

## 2. Features

- Color width selectable : 18/24/32/40 bit
- Single/Dual Link selectable
- Wide Frequency Range
- PLL requires no external components
- V-by-One ${ }^{\circledR} \mathrm{HS}$ standard version 1.5 compliant


Figure 1 THEVA215-V2 and THEVA216-V2 Top Side View

(a) THEVA215-V2 (Bottom Side)

(b) THEVA216-V2 (Bottom Side)

Figure 2 THEVA215-V2 and THEVA216-V2 Bottom Side View

## 4. Power Supply Set Up

This chapter shows power supply condition.
Caution: Check if there is no power-GND short on below red trace before supplying any power.

### 3.3V Power Supply to Each Board

Each evaluation board requires 3.3 V power supply. Use "CON1" connector typically.


Figure 3 Power Supply for Evaluation Board

## Power Supply from / to Connector

3.3 V power supply can be connected to Header1 and CON2 by using W1, W2 and W3solder jumper.

## THEVA215-V2

W1: Connect the 3.3 V power supply with pin\#1 and 2 of CON2.
W2: Connect the 3.3 V power supply with pin\#13 and 14 of CON3.
W3: Connect the 3.3 V power supply with pin\#11 and 12 of CON4.

(a)THEVA215-V2 (Top side)

(b) THEVA215-V2 (Bottom side)

Figure 4 THEVA215-V2 Power Supply from / to Each Connector

THEVA216-V2
W1: Connect the 3.3 V power supply with pin\#29 and 30 of CON2.
W2: Connect the 3.3 V power supply with pin\#1 and 2 of CON3.


Figure 5 THEVA216-V2 Power Supply from / to Each Connector

## 5. V-by-One ${ }^{\circledR}$ HS Input / Output Connector Select

V-by-One ${ }^{\circledR} \mathrm{HS}$ input / output connector can be selected by using 0ohm resistors.
(1) 1mm Pitch Connector (Default Setting)

Mount / unmount following 0ohm resistors to use 1 mm pitch connector.
Table 2

|  | Mount | Unmount |
| :---: | :---: | :---: |
| THEVA215-V2 | R7, R8, R9, R10, R11, R12 | R23, R25, R26, R31, R33, R34 |
| THEVA216-V2 | R12, R13, R14, R15, R16, R17 | R25, R26, R27, R29, R30, R31 |


(a)THEVA215-V2 (Top side)

(b)THEVA216-V2 (Top side)

Figure 6 Resistor Mounting for 1 mm Pitch Connector
(2) 0.5 mm Pitch Connector

Mount / unmount following 0ohm resistors to use 0.5 mm pitch connector.
Table 3

|  | Mount | Unmount |
| :---: | :---: | :---: |
| THEVA215-V2 | R23, R25, R26, R31, R33, R34 | R7, R8, R9, R10, R11, R12 |
| THEVA216-V2 | R25, R26, R27, R29, R30, R31 | R12, R13, R14, R15, R16, R17 |


(a)THEVA215-V2 (Bottom side)

(b)THEVA216-V2 (Bottom side)

Figure 7 Resistor Mounting for 0.5 mm Pitch Connector

## (3) SMA Connector

Mount / unmount following 0ohm resistors to use SMA connector.
*HTPDN and LOCKN signals don't have SMA connector input / output connection.
Table 4

|  | Mount | Unmount |
| :---: | :---: | :---: |
| THEVA215-V2 | R26, R31, R33, R34, R23, R25, R26, R31, R33, R34 | R7, R8, R9, R10, R11, R12 |
| THEVA216-V2 | R27, R29, R30, R31, R25, R26, R27, R29, R30, R31 | R12, R13, R14, R15, R16, R17 |



Figure 8 Resistor Mounting for SMA Connector

## 6. Function Setting

Setting pin of each board is shown in yellow area of figure9. Pin\#2 of each 3HEADER is connected to IC's setting pin.
Each setting pin's high or low setting can set by connecting pin\#2 of 3HEADER and high level or low level.


Figure 9 Position of Function Setting Pin


Figure 10 High / Low Setting Description

## 7. Clock Input from SMA Connector

THEVA215-V2 can also choose the TTL clock input from SMA connector, and it will be converted to LVDS clock signal by LVDS Buffer IC(SN65LVDS105S). If you want to use SMA connector for clock input, mount the IC and resistor as below.

Table 5


| R14 | 10kohm |
| :---: | :---: |
| R15 | 0ohm |
| R16 | 10kohm |
| R17 | 0ohm |
| R18 | 10kohm |
| R19 | 0ohm |
| R20 | 0ohm |
| R22 | 0ohm |
| R24 | 10kohm |

Figure 11 TTL Clock Input Connector Select
8. Status Indicate LED

The following table shows indicating status of each LED.
Table 6

|  | THEVA215-V2 | THEVA216-V2 |
| :---: | :---: | :---: |
| D1 | 3.3V Power Supply Indicator |  |
| D2 | LOCKN Status Indicator |  |

## 9. LOCKN Sharing, HTPDN Omission and Level Shift

## LOCK sharing

LOCKN connection can be shared with V-by-One ${ }^{\circledR} \mathrm{HS}$ trace. When you share the LOCKN signal, mount 1k ohm resistors to share the LOCKN signal, and unmount the 0ohm resistors shown in Figure 12.


Figure 12 LOCKN Sharing

## HTPDN Signal Omission

HTPDN signal can be omitted by using 1 k ohm resistor. When you omit the HTPDN signal, mount 1 k ohm resistors to pull down the HTPDN signal at transmitter side, and unmount the 0ohm resistors shown in Figure 13. When the HTPDN omission using, HTPDN output from receiver side is open connection.


Figure 13 HTPDN Signal Omission

## 10. Function

This chapter shows function setting of THEVA215-V2 and THEVA216-V2.

Table 7 THEVA215-V2 Function Setting Description

| Silk | Symbol | Function |
| :---: | :---: | :--- |
| PDN | PDN | Power down input. <br> H: Normal Operation <br> L: Power Down(CML output High Fix, other Hi-Z) |
| COL0,1 | COL0,1 | Color depth select input <br> L,L : 6bit <br> H, L : 8bit <br> L,H : 10bit <br> H,H : 12bit |
| SDSEL | SDSEL | Single/Dual select input <br> H: Channel0, Channel1 Enable <br> L: Channel0 Enable, Channel1 Disable |
| RRE1 | PRE1 | Pre emphasis level select input <br> H : 100\% L : 0\% |
| RES1 | Reserved1 | Field BET mode enable input <br> H : Field BET Mode Enable L : Normal Operation |

Table 8 THEVA216-V2 Function Setting Description

| Silk | Symbol | Function |
| :---: | :---: | :--- |
| COL0, 1 | COL0,1 | $\begin{array}{l}\text { Color depth select input } \\ \text { L,L : 6bit } \\ \text { H, L: 8bit } \\ \text { L,H : 10bit } \\ \text { H,H: 12bit }\end{array}$ |
| SDSEL | SDSEL | $\begin{array}{l}\text { Single/Dual select input } \\ \text { H: Channel0, Channel1 Enable } \\ \text { L: Channel0 Enable, Channel1 Disable }\end{array}$ |
| RS | RS | $\begin{array}{l}\text { Direction of RS pin depends on Reserved3. } \\ \text { LVDS swing range select input when Reserved3=L. } \\ \text { H: Normal Swing (350mV typ.) } \\ \text { L: Reduced Swing (200mV typ.) }\end{array}$ |
| Field BET output when Reserved3=H, Goes LOW when errors |  |  |
| detected. |  |  |\(\left.] \begin{array}{l}Power down input. <br>

H: Normal Operation <br>
L: Power Down(CML output High Fix, other Hi-Z)\end{array}\right]\)
11. Schematic


Figure 14 THEVA215-V2 Schematic


Figure 15 THEVA216-V2 Schematic

## 12. Bills of Materials

Table 9 THEVA215-V2 BOM

| TYPE | Value / Part No. | Package | SPEC | Reference No. | Q'ty | Note |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Capacitor | 10uF | 2012 | 16 V | C1, C2, C3, C4, C5, C6, C7, C8 | 8 |  |
| Capacitor | 4.7uF | 1005 | 16 V | C9, C10, C24 | 3 |  |
| Capacitor | 0.01uF | 1005 | 16 V | C11, C12, C13, C14, C19, C20, C21, C22, C23 | 9 |  |
| Capacitor | 0.1uF | 1005 | 16 V | C15, C16, C17, C18 | 4 |  |
| Connector | 282836-2(NC) | 5mm_pitch | 2pin | CON1 | 1 |  |
| Connector | 52271-3069(NC) | 1 mm _pitch | 30pin | CON2 | 1 |  |
| Connector | 52271-1469(NC) | 1 mm _pitch | 14pin | CON3 | 1 |  |
| Connector | CN-FFC(0.5)12PD(NC) | 0.5mm_pitch | 12pin | CON4 | 1 |  |
| Connector | SMA103-T16(NC) | 1.6 mm | PCB End Jack | SMA1, SMA2, SMA3, SMA4, SMA5 | 5 |  |
| Header | 3HEAD(NC) | 2.54 mm _pitch | --- | Header1, Header2, Header3, Header4, Header5, Header6 | 6 |  |
| IC | uPC2918BT | SC-63 | 1A | IC1 | 1 |  |
| IC | THCV215 | TSSOP64 | --- | IC2 | 1 |  |
| IC | SN65LVDS105D(NC) | TSSOP | 4V | IC3 | 1 |  |
| IC | SSM3K16FS | SSM | RON15 ${ }^{\text {a }}$ | U1 | 1 |  |
| Inductor | MPZ1608R471A | 1608 | 1.2A | L1, L2, L3, L4, L5 | 5 |  |
| LED0 | SML-310MT | 1608 | GREEN | D1,D2 | 1 |  |
| Resistor | $51 \Omega$ | 1005 | 0.1W | R2 | 1 |  |
| Resistor | $150 \Omega$ | 1005 | 0.1W | R1 | 1 |  |
| Resistor | $10 \mathrm{k} \Omega$ | 1005 | 0.1W | R3, R4 | 3 |  |
| Resistor | $1 \mathrm{k} \Omega$ ( NC ) | 1005 | 0.1W | R5, R6 | 2 |  |
| Resistor | $0 \Omega$ | 1005 | 1A | R7, R8, R9, R10, R11, R12, R30, R35, R36, R37 | 10 |  |
| Resistor | $0 \Omega(\mathrm{NC})$ | 1005 | 1A | R13, R15, R17, R19, R20, R21, R22, R23, R25, R26, R27, R28, R29, R31, R32, R33, R34, R38, R39 | 19 |  |
| Resistor | $10 \mathrm{k} \Omega(\mathrm{NC})$ | 1005 | 0.1W | R14, R16, R18, R24 | 4 |  |

Table 10 THEVA216-V2 BOM

| TYPE | Value / Part No. | Package | SPEC | Reference No. | Q'ty | Note |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Capacitor | 10uF | 2012 | 16 V | C1, C2, C3, C4, C5, C6, C7, C8, C9 | 9 |  |
| Capacitor | 4.7uF | 1005 | 16 V | C10, C11, C12, C27 | 4 |  |
| Capacitor | 0.01uF | 1005 | 16 V | C13, C14, C15, C16, C17, C22, C23, C24, C25, C26 | 10 |  |
| Capacitor | 0.1uF | 1005 | 16 V | C18, C19, C20, C21 | 4 |  |
| Connector | 282836-2(NC) | 5mm_pitch | 2pin | CON1 | 1 |  |
| Connector | 52271-3069(NC) | 1 mm _pitch | 30pin | CON2 | 1 |  |
| Connector | 52271-1469(NC) | 1 mm _pitch | 14pin | CON3 | 1 |  |
| Connector | CN-FFC(0.5)12PD(NC) | 0.5mm_pitch | 12pin | CON4 | 1 |  |
| Connector | SMA103-T16(NC) | 1.6 mm | PCB End Jack | SMA1, SMA2, SMA3, SMA4 | 4 |  |
| Header | 3HEAD(NC) | 2.54mm_pitch | --- | Header1, Header2, Header3, Header4, Header5, Header6 | 6 |  |
| IC | uPC2918BT | SC-63 | 1A | IC1 | 1 |  |
| IC | THCV216 | TSSOP64 | --- | IC2 | 1 |  |
| IC | SSM3K16FS | SSM | RON15 $\Omega$ | U1, U2, U3, U4 | 4 |  |
| Inductor | MPZ1608R471A | 1608 | 1.2A | L1, L2, L3, L4, L5, L6 | 6 |  |
| LED0 | SML-310MT | 1608 | GREEN | D1 | 1 |  |
| Resistor | $150 \Omega$ | 1005 | 0.1W | R1 | 1 |  |
| Resistor | $0 \Omega$ | 1005 | 1A | R2, R6, R12, R13, R14, R15, R16, R17, R23,R24, R25 | 10 |  |
| Resistor | $10 \mathrm{k} \Omega(\mathrm{NC})$ | 1005 | 0.1W | R3, R4, R9, R10 | 4 |  |
| Resistor | $0 \Omega(\mathrm{NC})$ | 1005 | 1A | $\begin{aligned} & \text { R5, R7, R8, R18, R19, R20, R21, R22, R26, R27, R28, R29, R30, } \\ & \text { R31, R32 } \end{aligned}$ | 16 |  |
| Resistor | $1 \mathrm{k} \Omega(\mathrm{NC})$ | 1005 | 0.1W | R11 | 1 |  |

13. Set Items

Table 11 Set Items

| TYPE | Part No. |
| :--- | :---: |
| DC Connector | $282836-2$ |
| FFC Connector for V-by-One ${ }^{\circledR}$ HS Link | $52271-1469$ |
| FFC 14pin 1mm pitch for V-by-One ${ }^{\circledR}$ HS Link | $98267-0299$ |
| Pin Header | --- |

It's possible to mount these parts on this board and use.

## 14. Notices and Requests

Please kindly read, understand and accept this "Notices and Requests" before using this product.

## For the Material:

1. The product specifications described in this material are subject to change without prior notice.
2. The circuit diagrams described in this material are examples of the application which may not always apply to design of respective customers. THine Electronics, Inc. ("THine") is not responsible for possible errors and omissions in this material. Please note even if the errors or omissions should be found in this material, THine may not be able to correct them immediately.
3. This material contains THine's copyright, know-how or other proprietary. Copying or disclosing of the contents of this material to any third party without THine's prior permission is strictly prohibited.

For the Product:

1. This product is solely designed for evaluation purpose, and other purposes including mass production and distribution are not intended.
2. This product has been solely manufactured for electric design engineers but not for end-users.
3. This product is not radiation-tolerant product.
4. This product is presumed to be used for general electric device, not for applications which require extremely high-reliability/safety (including medical device concerned with critical care, aerospace device, or nuclear power control device). Also, when using this product for any device concerned with control and/or safety of transportation means, traffic signal device, or other various types of safety device, such use must be after applying appropriate measures to the product.
5. This product has been designed with the utmost care to accomplish the purpose of evaluation of IC products manufactured by THine Electronics, Inc. ("THine"); however, THine MAKES NO WARRANTIES OR REPRESENTATIONS WITH REGARD TO ANY PERFORMANCE OR FUNCTION OF THIS PRODUCT IN ANY CIRCUMSTANCES.
6. This product has been manufactured with the utmost care in quality control and product reliability; however, there may be faults or defects with a low but fixed probability, as inevitable phenomenon concerned with semiconductor manufacturing processes. Therefore, customers are encouraged to have sufficiently redundant or error-preventive design applied to the use of the product so as not to have THine's product cause any social or public damage. Neither replacement nor failure analysis of the product is available in any case of defects with the product and/or the product's components.
7. Customers are asked, if required, to judge by themselves on whether this product falls under the category of strategic goods under the Foreign Exchange and Foreign Trade Act.
8. Please Note that even if infringement of any third party's industrial ownership should occur by using this product, THine will be exempted from any responsibility unless it directly relates to the production process or functions of the product.
9. Developing, designing and manufacturing of customers' own products, equipment or system by using of this product is strictly prohibited in any way.

THine Electronics, Inc.<br>https://www.thine.co.jp/

